

FIG. 1

SINGLE CLOCK BUS
SWITCHING SYSTEM

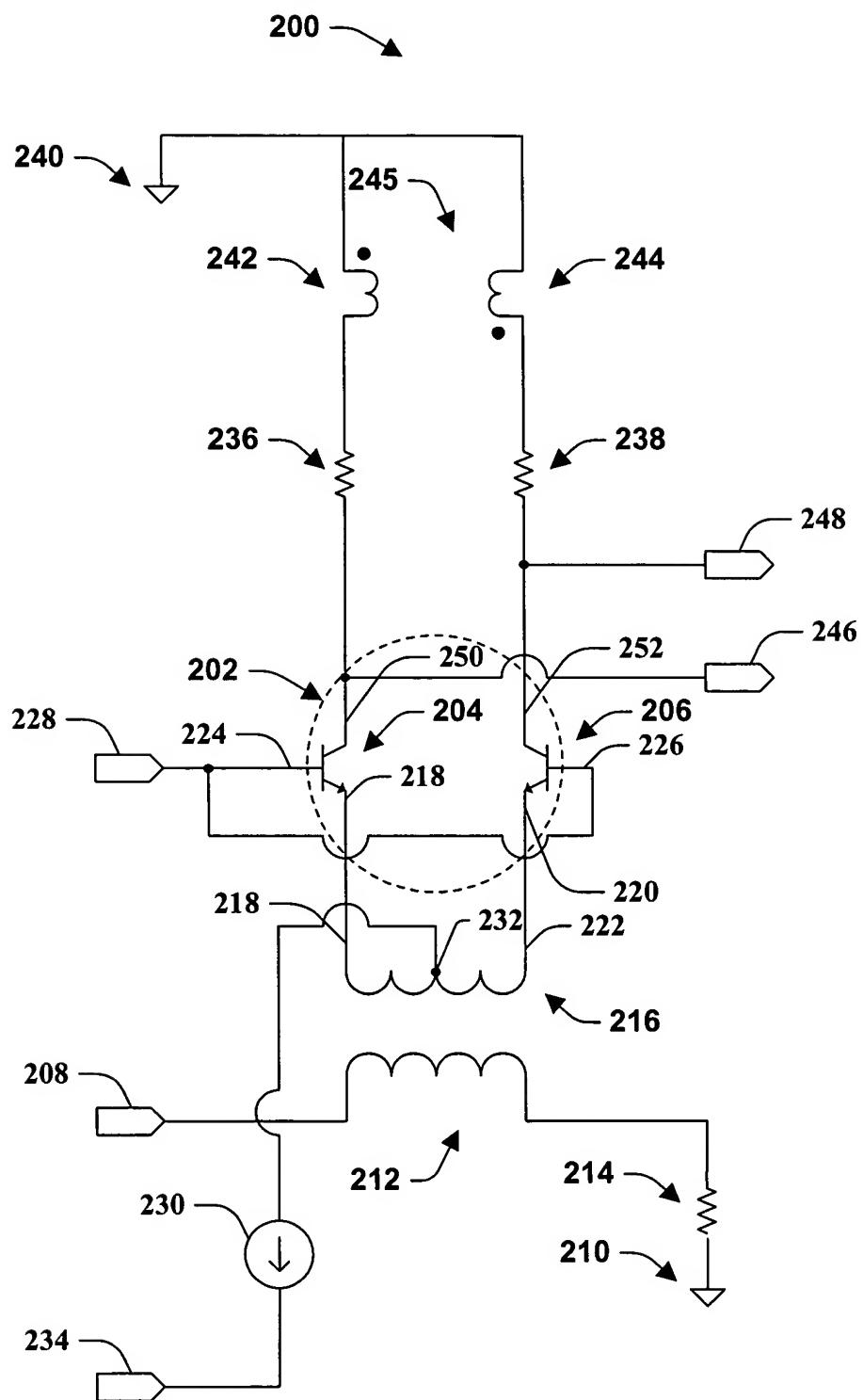
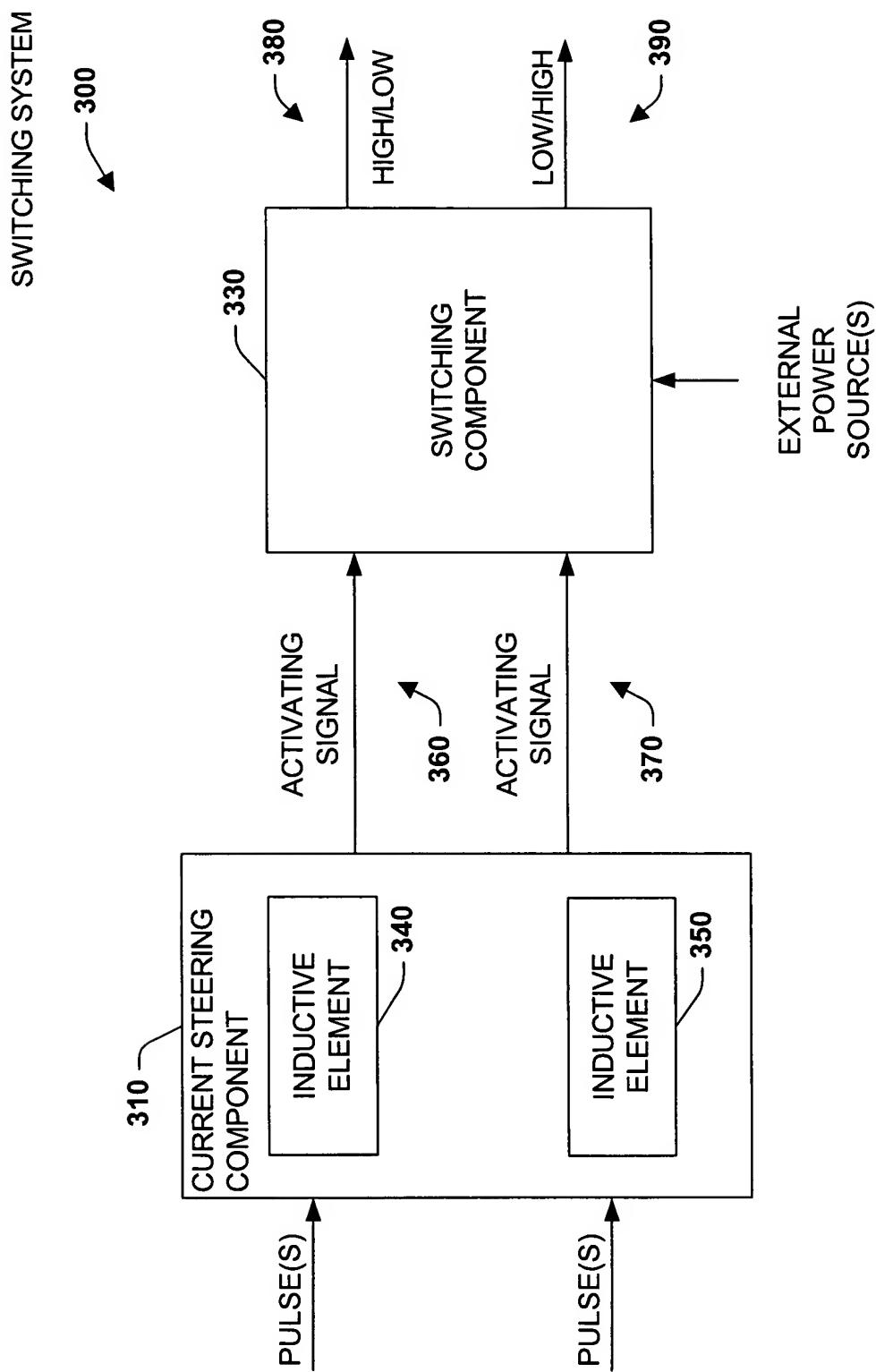


FIG. 2

FIG. 3



DIFFERENTIAL CLOCK BUS SWITCHING SYSTEM

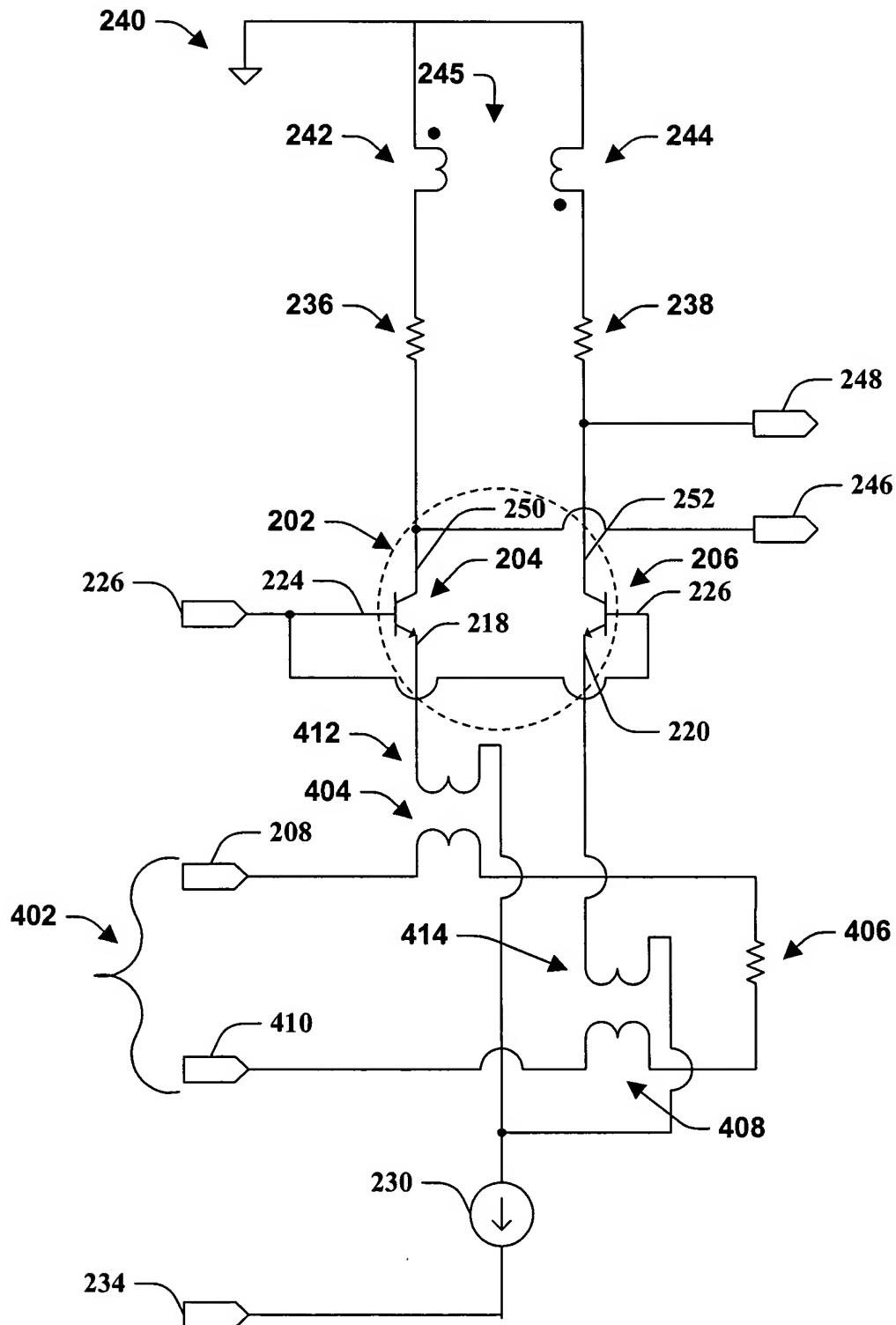


FIG. 4

500 → EXEMPLARY FLIP-FLOP

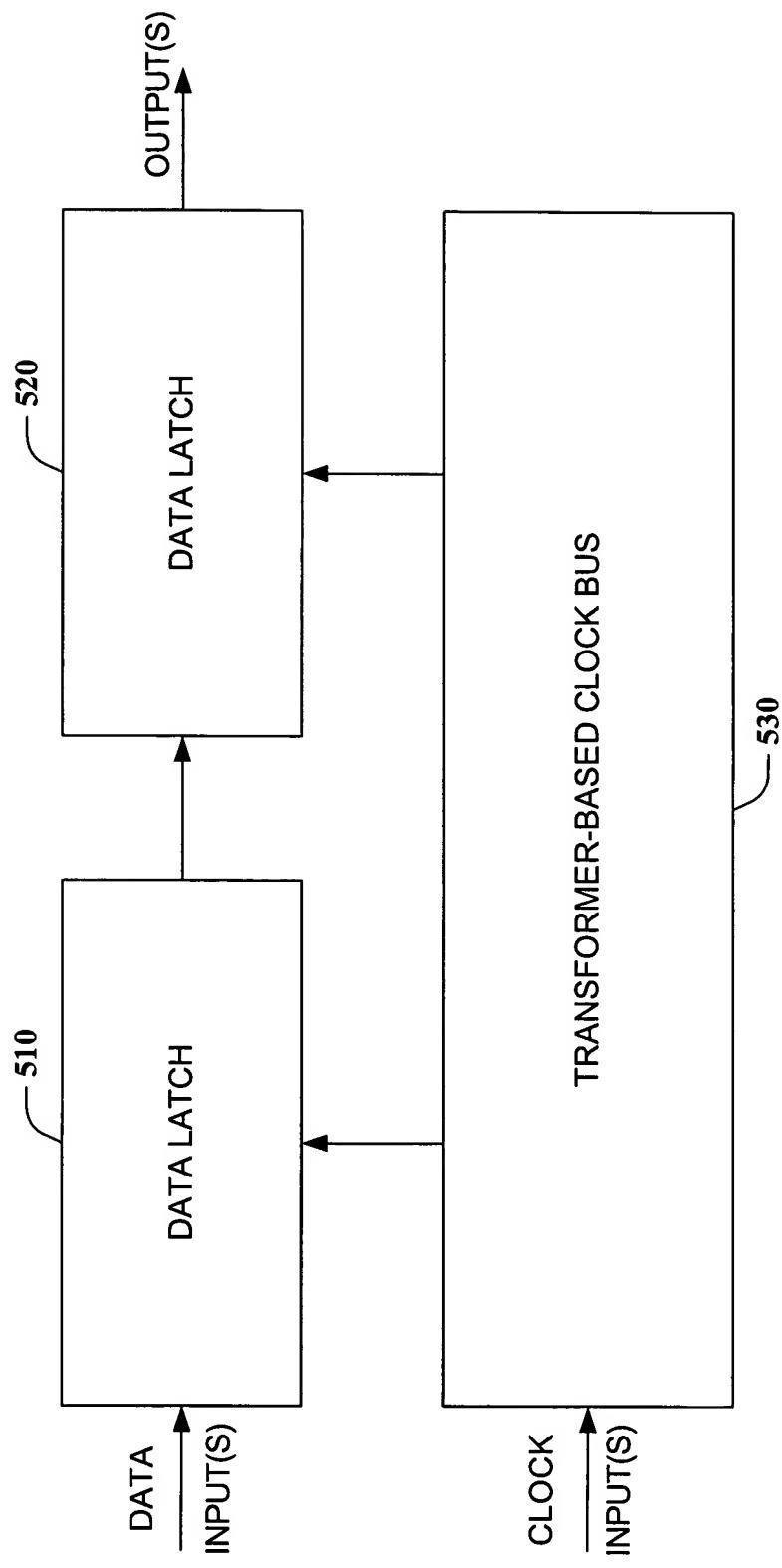


FIG. 5

600 → EXEMPLARY DATA TRACK
AND HOLD SYSTEM

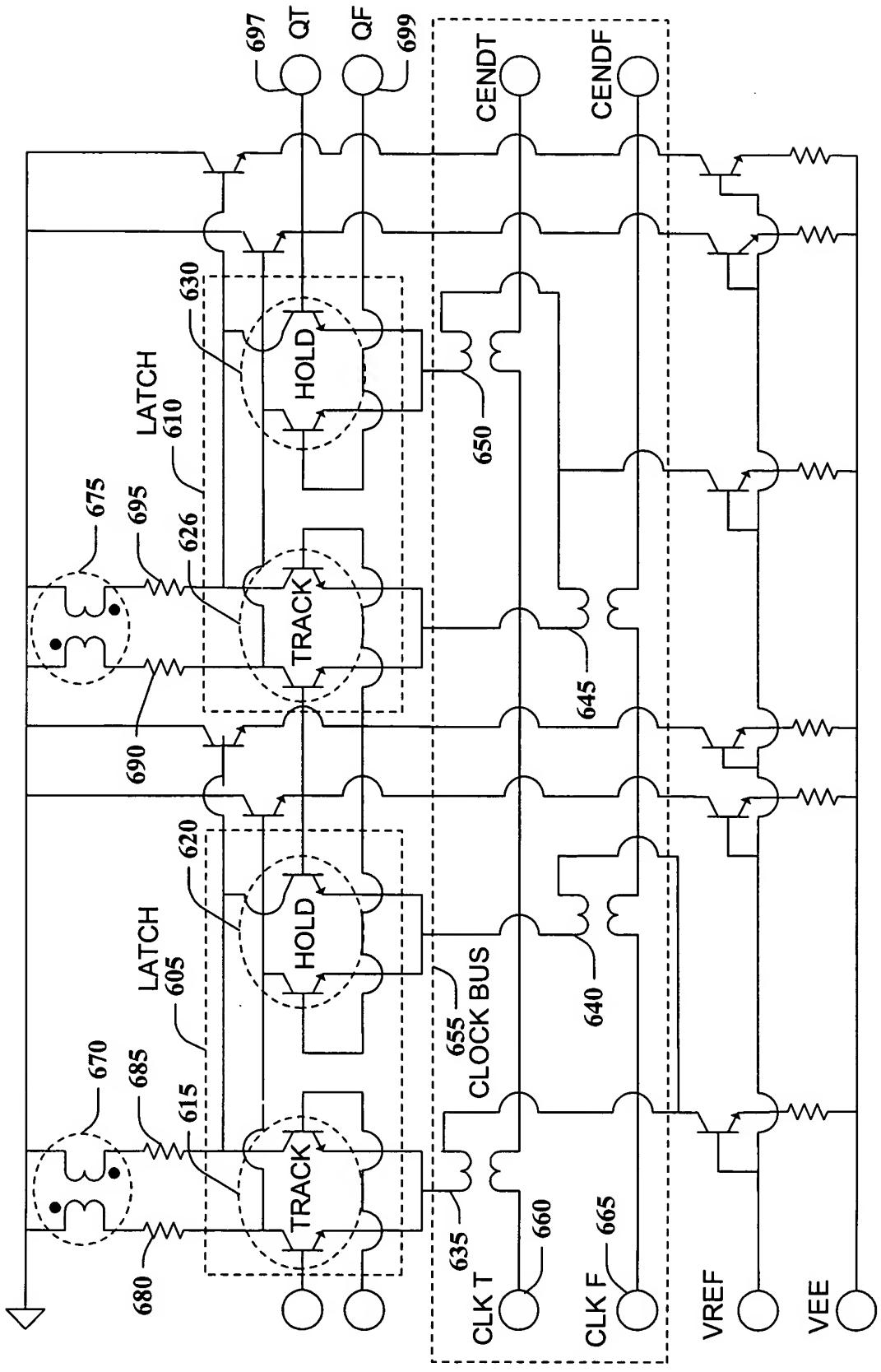


FIG. 6

X-BIT SHIFT REGISTER

700 ↘

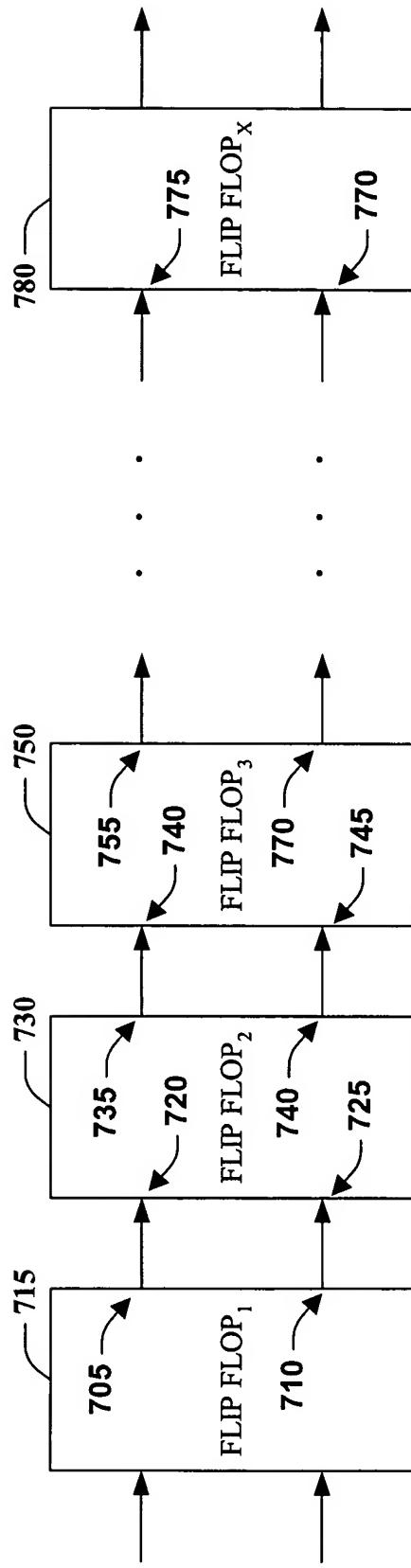


FIG. 7

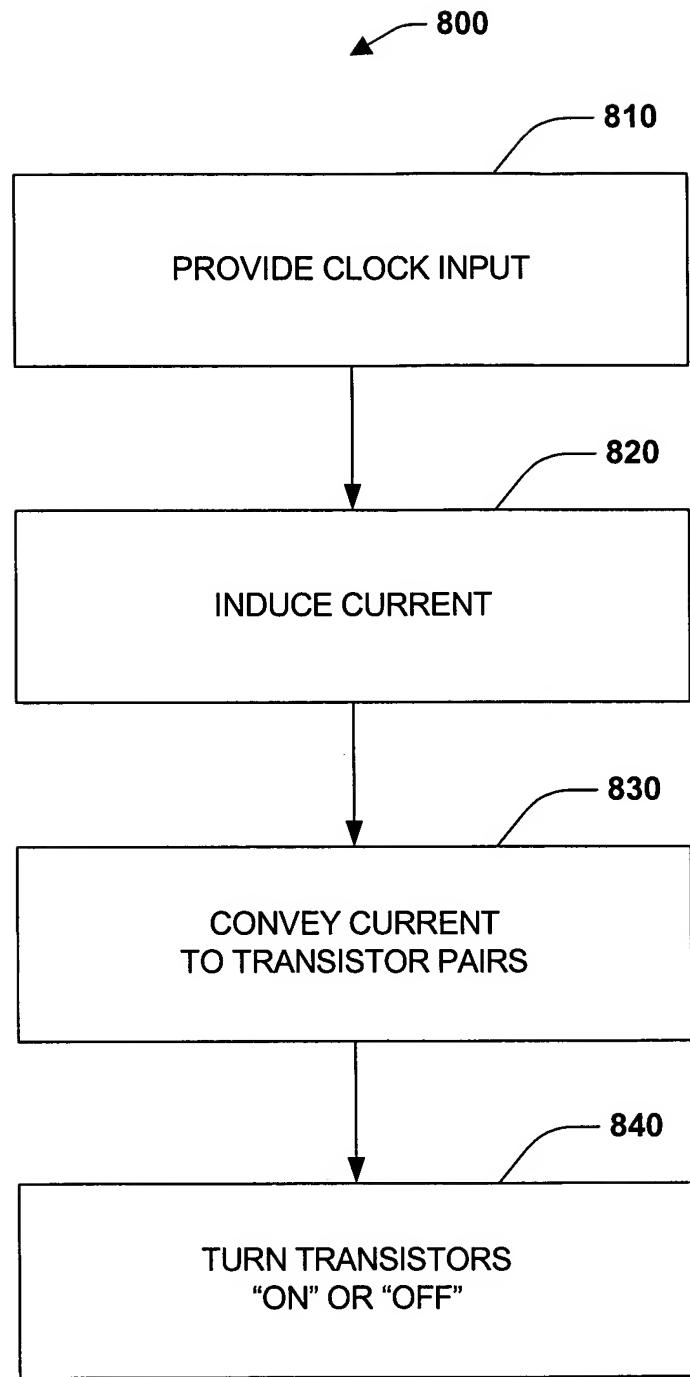


FIG. 8

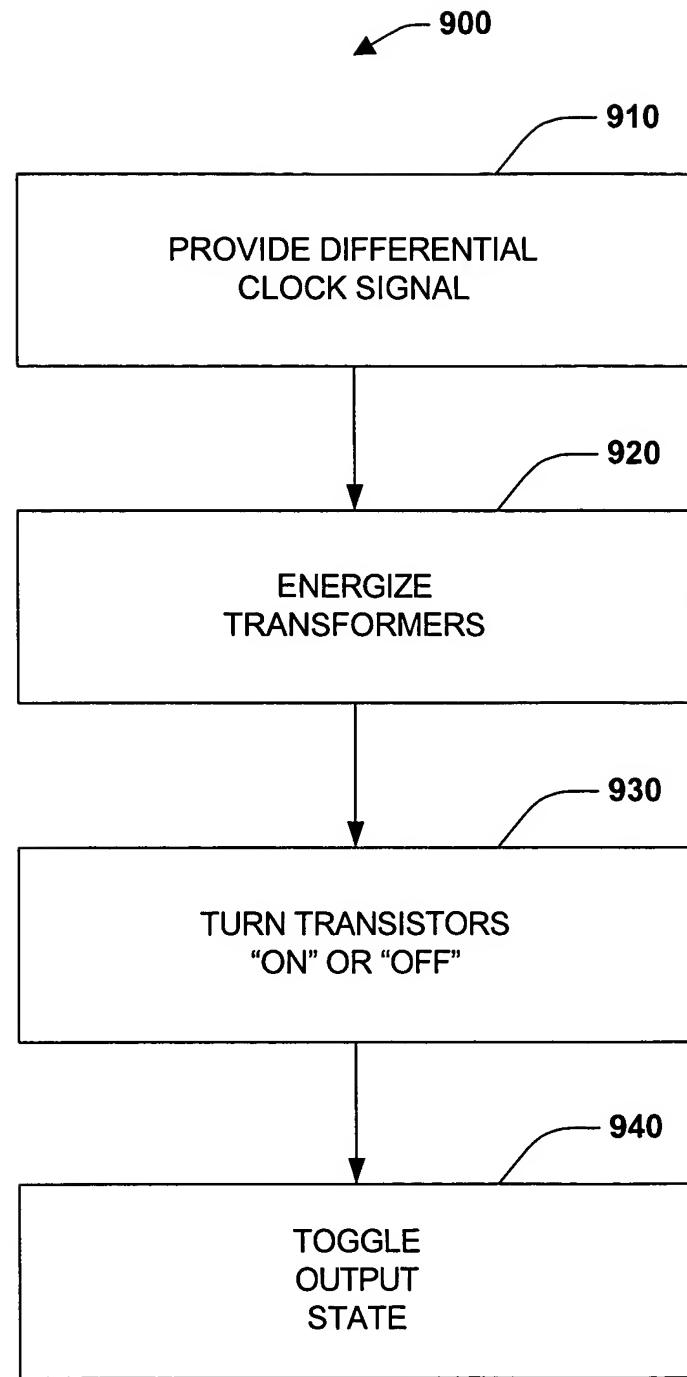


FIG. 9

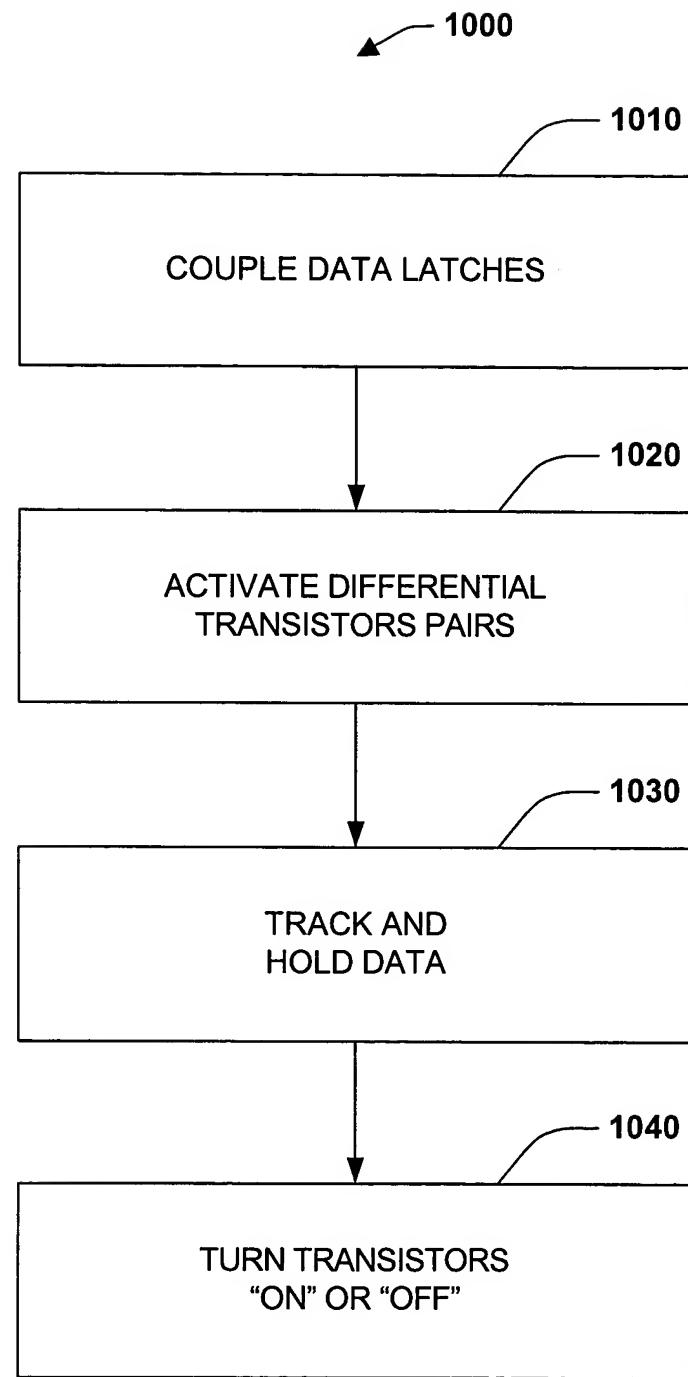


FIG. 10

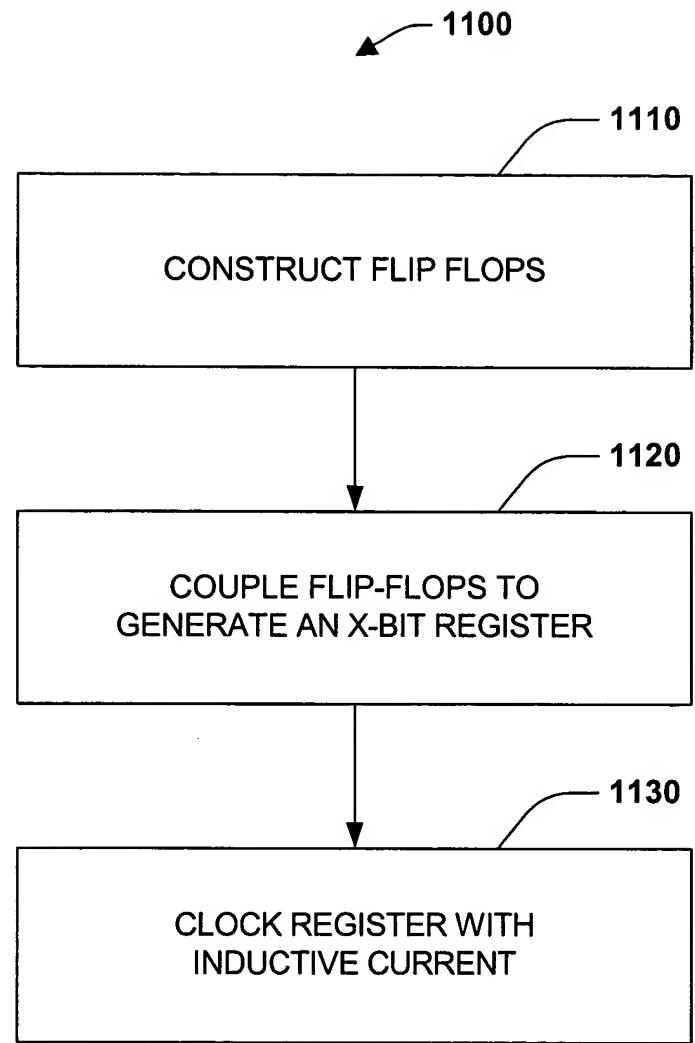


FIG. 11